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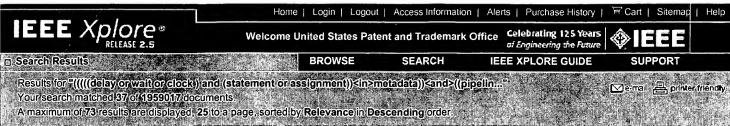
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	4.	Communication-sensitive loop scheduling for DSP applications Tongsima, S.; Sha, E.HM.; Passos, N.L.; Signal Processing, IEEE Transactions on [see also Acoustics, Speech, and Signal Processing, IEEE Transactions on] Volume 45, Issue 5, May 1997 Page(s):1309 - 1322 Digital Object Identifier 10.1109/78.575702	

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